

297-8991-511

DMS-100

# **XA-Core**

## Maintenance Guide

CSP17 Standard 07.02 February 2002

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## Maintenance Guide

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# Publication history

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## February 2002

Standard Release 07.02, coinciding with CSP17

- As of this release, this document has been discontinued. The information has been consolidated in other XA-Core documents. For instructions on where to find the information that was formerly in this document, see the “About this document” section.

## November 2001

Standard Release 06.02, supporting CSP16

- In the chapter titled “Introduction”, in the sections on shelf layouts, added information about the 7+1 PE configuration and about the high performance input/output circuit pack.
- In the chapter titled “Logs”, in the section on footprint logs, added a note regarding the FOOTPRT MAP level. Also in this chapter, added a section on time-of-day logs. Also in this chapter, in the table listing XA-Core log reports, added entries for logs XAC310 and XAC641.
- In the chapter titled “Operational measurements”, added two new HIOP-related OMs, XAHIOP and XAMDIPRT, to OM group XACORE.
- In the Chapter titled “User interface and commands”, updated the illustrations of the IO, RTIF, AMDI, and ETHR MAP levels. Also in this chapter, updated the information on RTIF commands: added the \LOADNAME command; deleted the \ENABLE command and the \SELF TEST command
- In the chapter titled “Cards and packlets”, added a section describing the high performance input/out processor (HIOP) circuit pack.
- In the chapter titled “Capacity-monitoring tools in an XA-Core”, added information explaining that the 1% CPU Allocation parameter is no longer supported in XA-Core.

### **April 2001**

Standard Release 05.01, supporting CSP15

- In the chapters titled “Introduction” and “Cards and packlets”, added information about the PE configurations supported in CSP15: 2+1, 3+1, and 5+1.

### **March 2001**

Standard Release 04.04

- Corrections to minor errors in Release 04.03.

### **March 2001**

Standard Release 04.03

- In the chapter titled “Capacity monitoring tools in an XA-Core”, added notes stating that when multiple PEs are out of service, the values displayed in certain fields on the Capacity MAP screen are incorrect and invalid.

### **October 2000**

Standard Release 04.02

- Content maintenance and revisions

### **August 2000**

Standard Release 04.01

- Added 3+1 configuration for PE cards, and the capability to provision up to 10 SM cards.
- Replaced “How to problem solve a store allocation error of a MemLim alarm” with the chapter titled “How to problem solve a MemLim alarm”.
- Added the Ethernet interface packlet and ETHR MAP display and commands required for trials of Succession Network IP solutions. Ethernet packlet is not scheduled for general availability until CSP15.
- Added the chapter titled “Capacity monitoring tools in an XA-Core”.

**May 2000**

Standard Release 03.02

- Added AMDI MAP level commands

**March 2000**

Standard release 03.01

- Updated processor element (PE) and shared memory configurations (SM)

**November 1999**

Standard release 02.02

- Add NTLX02CA processor element.

**August 1999**

Standard release 02.01

- Updates and format cleanup.

**June 1999**

Standard release 01.01

- First release of this document.



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## About this document

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To eliminate duplications in the XA-Core documentation, the information that was formerly found in the *XA-Core Maintenance Guide* has been consolidated in other XA-Core documents. These changes occurred as of the CSP17 release of the XA-Core documents. The following table indicates where to find the information that was formerly in the *XA-Core Maintenance Guide*.

**Table 1 Where to find the information that was formerly in this document**

<b>Former chapter in <i>XA-Core Maint. Guide</i></b>	<b>New location of the information</b>
"Introduction" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "Introduction" chapter
"Preventive maintenance methods" chapter	<i>XA-Core Maintenance Manual</i> , 297-8991-510, "Preventive maintenance" chapter
"Logs" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "Understanding XA-Core log reports" chapter
"Operational measurements" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "XA-Core operational measurements" chapter
"User interface and commands" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "XA-Core MAP levels and user interfaces" chapter
"Cards and packlets" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "XA-Core hardware description overview" chapter
"Problem isolation and correction" chapter	<i>XA-Core Maintenance Manual</i> , 297-8991-510, "Problem isolation and correction" chapter
"How to solve a MemLim alarm" chapter	<i>XA-Core Maintenance Manual</i> , 297-8991-510, "How to solve a MemLim alarm" chapter
"Capacity-monitoring tools in an XA-Core" chapter	<i>XA-Core Reference Manual</i> , 297-8991-810, "Capacity-monitoring tools in an XA-Core" chapter





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